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EXAMINATION INITIAL		DOCUMENT NO.	DATE	NAME		CLAS	CLASS CLAS		3	
B	AA	5,390,308	02-14-1995	Ware et al.						
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	AI									
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W	AM	Lin, Yu-Sheng and Shung, C. Bernard, "Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches", Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996.								
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LIST OF REFERENCES CITED BY APPLICANT

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*EXAMINER: